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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/562,101	03/13/2007	Sacha Romier	DE03 0228 US1	8087
65913	7590	10/01/2007	EXAMINER	
NXP, B.V.			HUYNH, PHUONG	
NXP INTELLECTUAL PROPERTY DEPARTMENT				
M/S41-SJ			ART UNIT	PAPER NUMBER
1109 MCKAY DRIVE			2857	
SAN JOSE, CA 95131				
			NOTIFICATION DATE	DELIVERY MODE
			10/01/2007	ELECTRONIC

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

ip.department.us@nxp.com

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<b>Office Action Summary</b>	<b>Application No.</b> 10/562,101	<b>Applicant(s)</b> ROMIER ET AL.	
	<b>Examiner</b> Phuong Huynh	<b>Art Unit</b> 2857	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) ☐ Responsive to communication(s) filed on 22 December 2005.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) ☒ Claim(s) 1-8 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-8 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 21 January 2007 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)                                | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                       | 5) <input type="checkbox"/> Notice of Informal Patent Application                       |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

## DETAILED ACTION

### *Claim Objections*

1. Claims 1, 3, 4, 5, and 8 are objected to because of the following informalities:

Claim 1, line 4; and claim 4, line 5, "the actual temperature" lacks proper antecedent basis. No "actual temperature" was previously recited.

Claim 3, line 3, "the temperature extraction unit" lacks proper antecedent basis. No "temperature extraction unit" was previously recited.

Claim 5, line 2, "the resulting temperatures" lacks proper antecedent basis. No "resulting temperatures" was previously recited.

Claim 8, line 2, --the-- should be added before "first signal."

Appropriate correction is required.

### *Claim Rejections - 35 USC § 102*

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

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Claims 1-8 are rejected under 35 U.S.C. 102(b) as being anticipated by Holloway et al. (hereinafter "Holloway") (US Patent No. 6,183,131).

Regarding claim 1, Holloway discloses an arrangement on a semiconductor chip for calibrating a temperature setting curve having

a signal generation unit for providing a first signal which is proportional to the actual temperature of the chip, whereby a signal offset creatable by the signal generation unit, which is combined with the first signal defining second signal [see Holloway: col. 11, lines 15-50];

a signal extraction unit [A/D and summing circuits 114] receiving the first signal and the second signal calculating a first temperature point based on the first signal and a second temperature point based on the second signal [see Holloway: col. 7, lines 7-67, lines col. 11, line 40-col. 12, line 6].

Regarding claim 2, Holloway discloses that the first signal which is proportional to the actual temperature of the chip, is a current, voltage or a frequency [see Holloway: col. 11, lines 15-40].

Regarding claim 3, Holloway discloses that the first signal and the second signal are convertible into digital signals, whereby the extraction unit calculates the first and second temperature points for calibrating the temperatures setting curves [see Holloway: col. 11, line 40-col. 12, line 6].

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Regarding claim 4, Holloway discloses a method for calibrating a temperature setting curve of a temperature sensor arrangement on a semiconductor chip, the method comprising: reading a first signal which is proportional to the actual temperature of the chip [see Holloway: col. 11, lines 15-40]; generating a signal offset, which is combined with the first signal defining a second signal [see Holloway: col. 11, lines 40-47]; extracting a first temperature from the first signal and a second temperature from the second signal [see Holloway: col. 7, lines 7-67; col. 11, line 45-col. 12, line 25].

Regarding claim 5, Holloway discloses whereby resulting temperatures are used for providing calibration parameters to the chip [see Holloway: col. 7, lines 7-67; col. 11, line 45-col. 12, line 6].

Regarding claim 6, Holloway discloses whereby calculating calibration parameters can be performed on-chip or off-chip [see Holloway: Abstract; col. 7, lines 7-67; col. 11, line 45-col. 12, line 6].

Regarding claim 7, Holloway discloses whereby additional offsets are provided for calculating more than two temperature points and calibrating a non-linear temperature setting curve [see Holloway: col. 2, lines 6-20; col. 11, line 40-col. 12, line 25]. In addition, Examiner reminds Applicant that "calculating more than two temperature points" is duplicate part for multiple effects and this generally does not provide patentable weight to the claimed invention. See *St. Regis Paper Co. v Bemis Co.* 193 USPQ 8 (7<sup>th</sup> Cir. 1977).

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Regarding claim 8, Holloway discloses that whereby the signal offset is subtracted from the first signal or added to the first signal defining a second signal, which is provided to the temperature extraction unit [see Holloway: col. 11, line 45-col. 12, line 25].

### ***Conclusion***

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phuong Huynh whose telephone number is 571-272-2718. The examiner can normally be reached on M-F: 8:30 AM - 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eliseo Ramos-Feliciano can be reached on 571-272-7925. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Phuong Huynh  
Examiner  
Art Unit 2857

PH September 19, 2007

